

This listing of claims will replace all prior versions, and listings, of claims in the application.

**Listing of Claims:**

1. (Cancelled)
2. (Currently Amended) The method as recited in claim 10 ~~+~~, further comprising if the computing architecture comprises a human interface portal, displaying a first rectangle stacked adjacent to the first plurality of bars, the first rectangle representing a human interface portal.
3. (Currently Amended) The method as recited in claim 10 ~~+~~, further comprising: if the computing architecture comprises a business to business integration, displaying a second rectangle stacked adjacent to the first plurality of bars, the second rectangle representing a business to business integration.
4. (Currently Amended) The method as recited in claim 10 ~~+~~, further comprising displaying an icon to indicate a preselected area of the computing architecture.
5. (Currently Amended) The method as recited in claim 10 ~~+~~, further comprising: identifying a data store of the computing architecture; and displaying an icon to represent the identified data store.
6. (Original) The method as recited in claim 5 further comprising: identifying a computing system corresponding to the data store; and displaying the data store proximate a bar corresponding to the data store.
7. (Currently Amended) The method as recited in claim 10 ~~+~~ further comprising: identifying a plurality of data stores; identifying interconnections between the plurality of data stores; displaying each of the plurality of data stores as an icon; and

displaying line interconnecting the icons, the lines representing the identified interconnections between the plurality of data stores.

8. (Currently Amended) The method as recited in claim 10 ~~+~~, further comprising identifying computing systems having a predefined computing function and displaying an icon to represent the predefined computing function.

9. (Currently Amended) The method as recited in claim 10 ~~+~~, wherein displaying a first plurality of bars comprises displaying, from top to bottom, a horizontal bar for each of a presentation layer, a business logic layer, a data layer, and an infrastructure layer.

10. (Currently Amended) A method for modeling a computing architecture, the method executed on a computer, the method comprising:

identifying a plurality of computing layers, each computing layer representing a functional layer of the computing architecture;

identifying a plurality of computing systems;

identifying, for each of the plurality of computing systems, at least one of the plurality of computing layers that is implemented by the computing system;

displaying a first plurality of bars in stacked relationship to each other, each of the first plurality of bars representing a computing layer of the plurality of computing layers;

displaying a second plurality of bars approximately orthogonal to the first plurality of bars and over at least a portion of the first plurality of bars, each of the second plurality of bars representing a computing system, wherein each of the second plurality of bars extends through a portion of the first plurality of bars an amount corresponding to the identified at least one of the computing layers that is implemented by the computing system represented by the bar; and

~~The method as recited in claim 1, further comprising~~ displaying a selected one of the first plurality of bars larger than the other bars for displaying details within the selected bar.

11. (Currently Amended) The method as recited in claim 10 ~~1~~, further comprising displaying a conduit within one of the first plurality of bars, the conduit representing communication links between computing systems.

12. (Currently Amended) The method as recited in claim 10 ~~1~~, further comprising:  
receiving a selection of a computing system of the computing architecture;  
displaying a first plurality of bars in stacked relationship to each other, each of the first plurality of bars representing a computing layer comprised in the selected computing system; and

displaying a second plurality of bars approximately orthogonal to the first plurality of bars and over at least a portion of the first plurality of bars, each of the second plurality of bars representing a sub-system of the selected computing system, wherein each of the second plurality of bars extends through a portion of the first plurality of bars an amount corresponding to the identified at least one of the computing layers that is implemented by the sub-system of the selected computing system represented by the bar.

13. (Cancelled)

14. (Currently Amended) The computer-readable medium as recited in claim 17 ~~1~~, wherein the instructions further cause the processor to perform:

if the computing architecture comprises a human interface portal, displaying a first rectangle stacked adjacent to the first plurality of bars, the first rectangle representing a human interface portal.

15. (Currently Amended) The computer-readable medium as recited in claim 17 ~~1~~, wherein the instructions further cause the processor to perform:

if the computing architecture comprises a business to business integration, displaying a second rectangle stacked adjacent to the first plurality of bars, the second rectangle representing a business to business integration.

16. (Currently Amended) The computer-readable medium as recited in claim 17 ~~4~~, wherein the instructions further cause the processor to perform displaying an icon to indicate a preselected area of the computing architecture.

17. (Currently Amended) A computer-readable medium having instructions stored thereon for modeling a computing architecture, the instructions, when executed on a processor, causing the processor to perform the following:

identifying a plurality of computing layers, each computing layer representing a functional layer of the computing architecture;

identifying a plurality of computing systems;

identifying, for each of the plurality of computing systems, at least one of the plurality of computing layers that is implemented by the computing system;

displaying a first plurality of bars in stacked relationship to each other, each of the first plurality of bars representing a computing layer of the plurality of computing layers;

displaying a second plurality of bars approximately orthogonal to the first plurality of bars and over at least a portion of the first plurality of bars, each of the second plurality of bars representing a computing system, wherein each of the second plurality of bars extends through a portion of the first plurality of bars an amount corresponding to the identified at least one of the computing layers that is implemented by the computing system represented by the bar; ~~The computer-readable medium as recited in claim 1, wherein the instructions further cause the processor to perform:~~

~~identifying a data store of the computing architecture; and~~

~~displaying an icon to represent the identified data store.~~

18. (Original) The computer-readable medium as recited in claim 17 wherein the instructions further cause the processor to perform:

identifying a computing system corresponding to the data store; and

displaying the data store proximate a bar corresponding to the data store.

19. (Currently Amended) The computer-readable medium as recited in claim 17 ~~4~~ wherein the instructions further cause the processor to perform:

identifying a plurality of data stores;  
identifying interconnections between the plurality of data stores;  
displaying each of the plurality of data stores as an icon; and  
displaying line interconnecting the icons, the lines representing the identified interconnections between the plurality of data stores.

20. (Currently Amended) The computer-readable medium as recited in claim 17 ~~4~~, wherein the instructions further cause the processor to perform identifying computing systems having a predefined computing function and displaying an icon to represent the predefined computing function.

21. (Currently Amended) The computer-readable medium as recited in claim 17 ~~4~~, wherein displaying a first plurality of bars comprises displaying, from top to bottom, a horizontal bar for each of a presentation layer, a business logic layer, a data layer, and an infrastructure layer.

22. (Currently Amended) A computer-readable medium having instructions stored thereon for modeling a computing architecture, the instructions, when executed on a processor, causing the processor to perform the following:

identifying a plurality of computing layers, each computing layer representing a functional layer of the computing architecture;

identifying a plurality of computing systems;

identifying, for each of the plurality of computing systems, at least one of the plurality of computing layers that is implemented by the computing system;

displaying a first plurality of bars in stacked relationship to each other, each of the first plurality of bars representing a computing layer of the plurality of computing layers;

displaying a second plurality of bars approximately orthogonal to the first plurality of bars and over at least a portion of the first plurality of bars, each of the second plurality of bars representing a computing system, wherein each of the second plurality of bars extends through a portion of the first plurality of bars an amount corresponding to the identified at least one of the computing layers that is implemented by the computing system represented

~~by the bar; and The computer-readable medium as recited in claim 1, wherein the instructions further cause the processor to perform~~ displaying a selected one of the first plurality of bars larger than the other bars for displaying details within the selected bar.

23. (Currently Amended) The computer-readable medium as recited in claim 22 ~~1~~, wherein the instructions further cause the processor to perform displaying a conduit within one of the first plurality of bars, the conduit representing communication links between computing systems.

24. (Currently Amended) The computer-readable medium as recited in claim 22 ~~1~~, wherein the instructions further cause the processor to perform:

receiving a selection of a computing system of the computing architecture;

displaying a first plurality of bars in stacked relationship to each other, each of the first plurality of bars representing a computing layer comprised in the selected computing system; and

displaying a second plurality of bars approximately orthogonal to the first plurality of bars and over at least a portion of the first plurality of bars, each of the second plurality of bars representing a sub-system of the selected computing system, wherein each of the second plurality of bars extends through a portion of the first plurality of bars an amount corresponding to the identified at least one of the computing layers that is implemented by the sub-system of the selected computing system represented by the bar.